Chettinad College of Engineering & Technology, Karur

Department of Electronics and Communication Engineering

News Report

Programme Name: Value Added Course on VLSI Design
Faculty Incharge : : Ms.D.Ragavi, AP/ECE
                               Mrs.P.Nagarani Sobana, AP/ECE
Date: 24.07.2023 to 26.07.2023
Venue: DSP and VLSI Laboratory
Number of Participants: 43
Description:

Our ECE Department was successfully conducted "Three Day Value Added Course- VLSI Design "from 24.07.2023 to 26.07.2023. The main objective of this VLSI Design course was conducted to know the concept of fundamentals of VHDL coding and also various modelling types like structural , behavioural, dataflow , schematic of VLSI design with the help of Xilinx and Microwind tools . By this session , we give training on how to write the coding for the given concept with syntax and by use of Xilinx tools they use to learn synthesis, place and map report , behavioural simulation, design implementation and test bench analysis . All the IIIrd year ECE students were actively participated and at the end of this course, the students have learned and executed the various concepts from digital circuits like basic gates, combinational like Multiplexer, Demultiplexer , encoding , decoding part and sequential circuits such as various flipflop design with simulation and also done various CMOS logic design .

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