

# Chettinad College of Engineering & Technology, Karur

## Department of Electronics and Communication Engineering

### News Report

**Programme Name:** Six Day VLSI Level-1 Certification Course

**Resource Person(s):**

**Mr.R.Venkatesh, Trainer**

**TARAS Systems and Solutions, Coimbatore.**

**Faculty Incharge : Mrs.A.Karthikeyani, AP/ECE**

**Date : 12.08.2024 to 17.08.2024 (Six Days)**

**Venue : DSP & VLSI Laboratory**

**Number of Participants : 59**

#### Description:

Our Department of Electronics and Communication Engineering successfully conducted a “Six Day VLSI Level-1 Certification Course” exclusively for our III year ECE Students handled by TARAS Systems and Solutions from 12.08.2024 to 17.08.2024 (Six Days). The course focused on foundational concepts in digital design using Verilog and System Verilog. It provided a blend of theoretical knowledge and practical skills essential for understanding and implementing digital circuits using the tool EDA (Open Source). The course covered essential modules like VLSI Technology, Verilog HDL, Combinational and Sequential Logic Design, FSM Model, Memory Design and Synthesis. Students were assessed through Assignments, Quiz and a final project. After the completion of this course, the students learned how to design and simulate complex digital circuits and debug System Verilog code, work on Real world VLSI projects for solving practical problems in the semiconductor industry

**Photos:**

